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Transmission Date:

June 8, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Low, et al.

Docket No.:

02 P 15173 US / INTECH 3.

Serial No:

10/689,233

Art Unit:

2891

Date Filed:

October 20, 2003

Examiner:

Asok K. Sarkar

Title:

Inclusion of Low-k Dielectric Material between Bit Lines

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (571) 273-2885 on the date shown above:

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Respectfully submitted,

Kristin R. Hay Legal Assistant

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